

































ALL CONTROLS MAX. EXCEPT CH. B LEVEL @ 5 AND REV. A & B @ 0
INPUT SIGNAL 10mV @ 1kHz
LINE VOLTAGE 120VAC @ 60Hz
ALL DC VOLTAGES WITH NO SIGNAL APPLIED

CHANNEL A (CLEAN CHANNEL)			CHANNEL B (DIRTY CHANNEL)			
T.P.	AC VOLTAGE	DC VOLTAGE	WAVEFORM	AC VOLTAGE	DC VOLTAGE	WAVEFORM
12	.8V p-p	-4.6V		1.2V p-p	-4.6V	
13	21V p-p	171V		23.5V p-p	171V	
14	20V p-p	170V		26V p-p	170V	
15	—	27V	—	—	27V	—
16	6.6V p-p	-275V		10V p-p	-275V	
17	—	9.7V	—	—	9.7V	—
18	.7V p-p	307V		.7V p-p	307V	
19	10V p-p	317V		10V p-p	317V	
20	40V p-p	—		42V p-p	—	
21	10V p-p	317V		10V p-p	317V	
22	.2V p-p	-3V		.2V p-p	-3V	
23	.2V p-p	3V		.2V p-p	3V	
24	8V p-p	—		8V p-p	—	
25	8V p-p	—		8V p-p	—	
26	.7V p-p	15.4V		.7V p-p	15.4V	
27	.7V p-p	-15.4V		.7V p-p	-15.4V	
28	10mV p-p	10.4V		10mV p-p	10.4V	
29	10mV p-p	-9.9V		10mV p-p	-9.9V	

REV. LOADED WITH 150 ohm RESISTOR
SPKR. LOADED WITH 8 ohm RESISTOR

SEE SHEET 1 OF 2 FOR REVISION HISTORY

SIGNATURES:		DATE:		11880 BORMAN DR. ST. LOUIS, MISSOURI 63146	
DRAWN:	MGA	11/07/93		PROJECT NAME:	VC-3112/B
CHK'D:	OK	11/08/93		DRAWING NAME:	SCHEMATIC DRAWING
APP'D:	OK	11/08/93		DRAWING NO.	07S269-01
ORIGINAL ISSUED:	11/07/93			REV.	10
PLOT DATE:	01/08/99			FILE NAME:	26901hk...
PLOT TIME:	10:20:52			SCALE:	~
				SHEET:	2 OF 2